



PATENT APPLICATION
Do. No. 5038-062
Intel Reference No. P9612

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: John B. Halbert and Randy M. Bonella

Serial No. 09/666,528

Examiner: Huan Hoang

Filed: September 18, 2000

Group Art Unit: 2818

For: MEMORY SYSTEM HAVING BUFFERS FOR ISOLATING
STACKED MEMORY DEVICES

Date: September 6, 2001

BOX FEE AMENDMENT

Assistant Commissioner for Patents
Washington, DC 20231

6/10 Shaeley
9/17/01
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SEP 17 2001

RESPONSE TO OFFICE ACTION

Responsive to the Office Action, dated June 6, 2001, please amend the application as follows.

In The Specification

In the first line of the specification following the title of the invention, please add the following:

"This application claims priority from U.S. Provisional Application Ser. No. 60/232,596 filed September 14, 2000 titled "Memory System Having Buffers For Isolating Stacked Memory Devices."

In The Claims

Please amend the claims by rewriting as follows:

1. (Amended once) A memory system comprising:
a first memory device;
a second memory device stacked on the first memory device;
a first buffer coupled to the first and second memory devices;

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